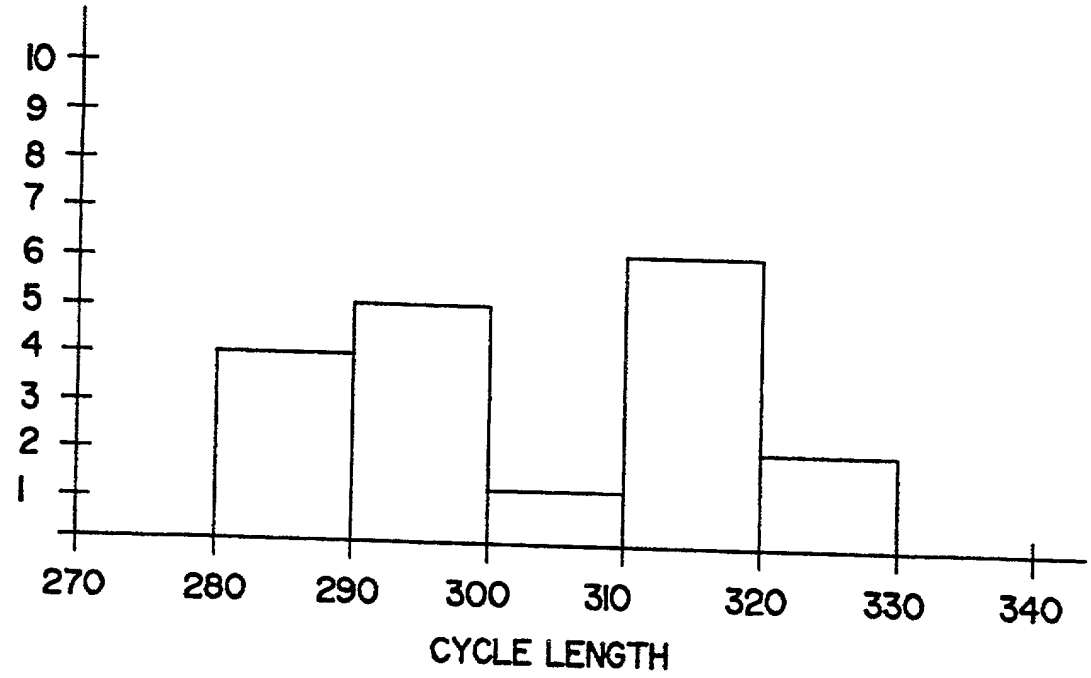

FIG. 1

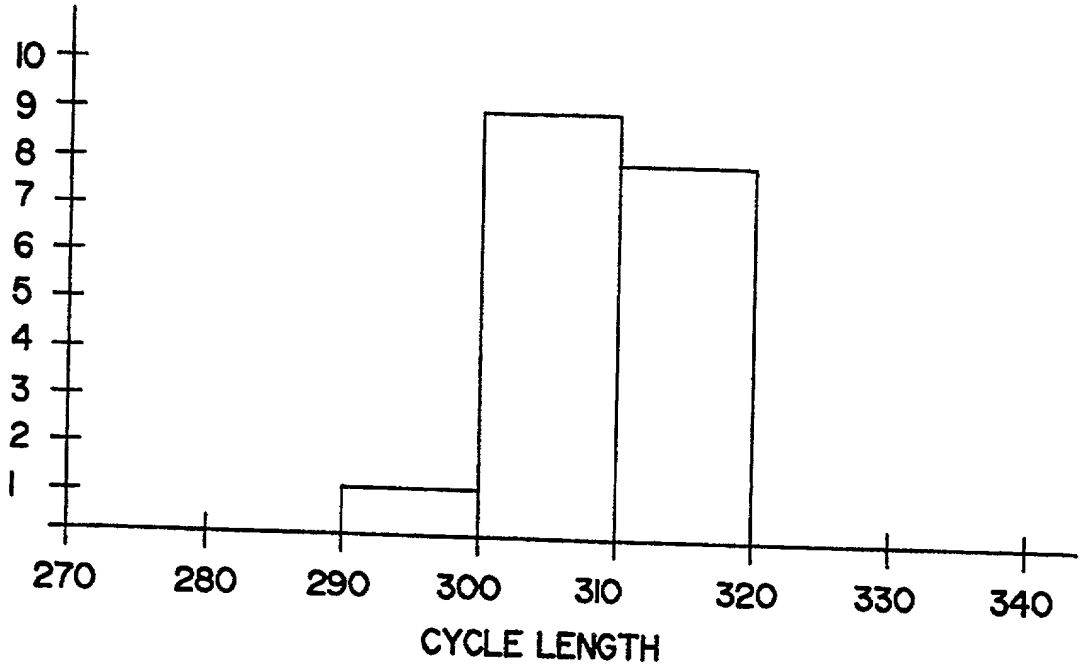

FIG. 2

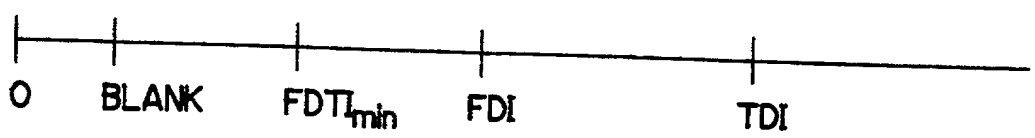

FIG. 3

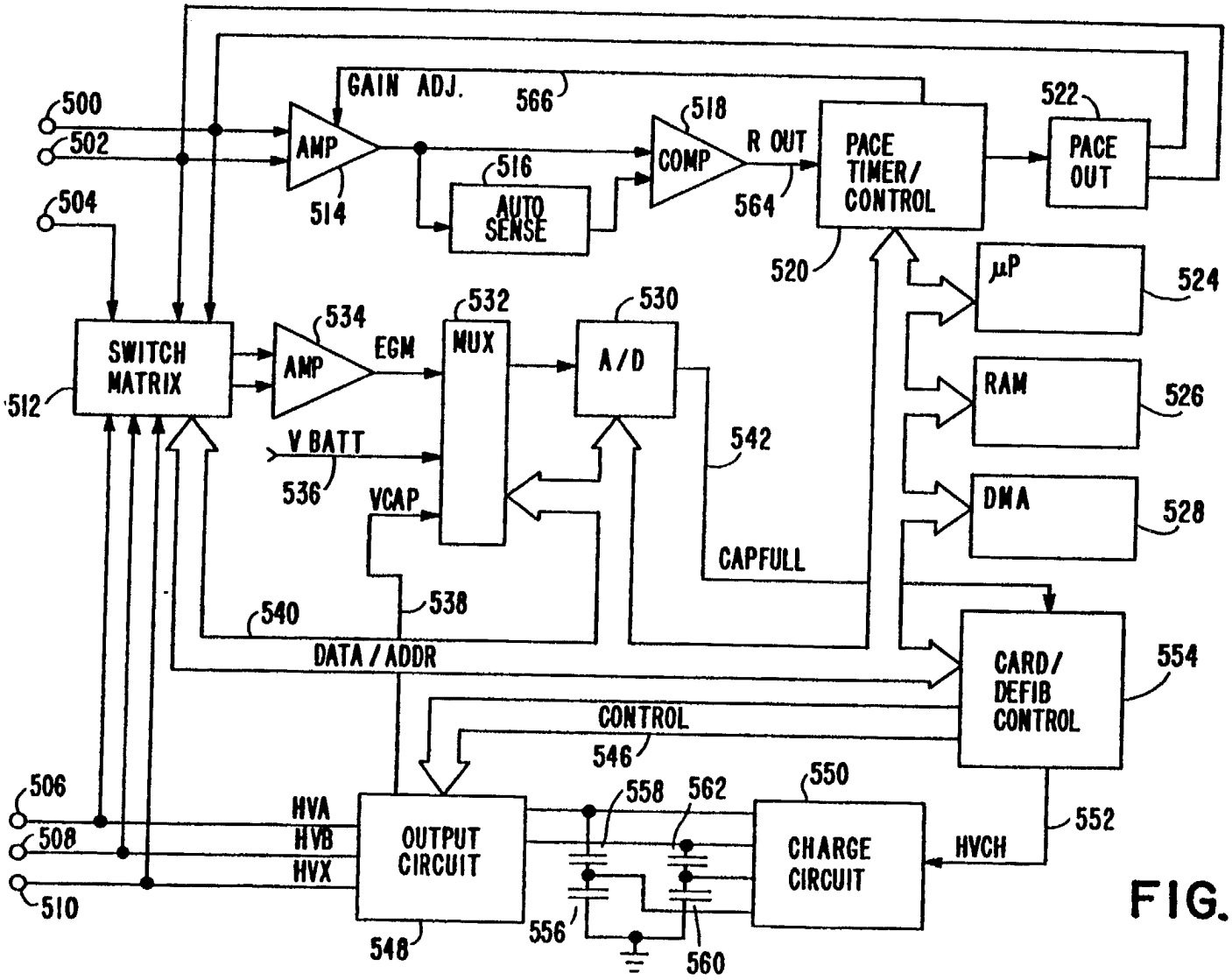
GAIN ADJ.
566
500
502
504
AMP
514
516
AUTO SENSE
518
COMP
R OUT
564
PACE TIMER/ CONTROL
520
522
PACE OUT
µP
524
RAM
526
DMA
528
SWITCH MATRIX
512
534
AMP
EGM
532
MUX
530
A/D
542
V BATT
536
VCAP
CAPFULL
540
538
DATA / ADDR
CARD/ DEFIB CONTROL
554
CONTROL
546
506
508
510
HVA
HVB
HVX
OUTPUT CIRCUIT
548
558
562
556
560
550
CHARGE CIRCUIT
HVCH
552

FIG. 4

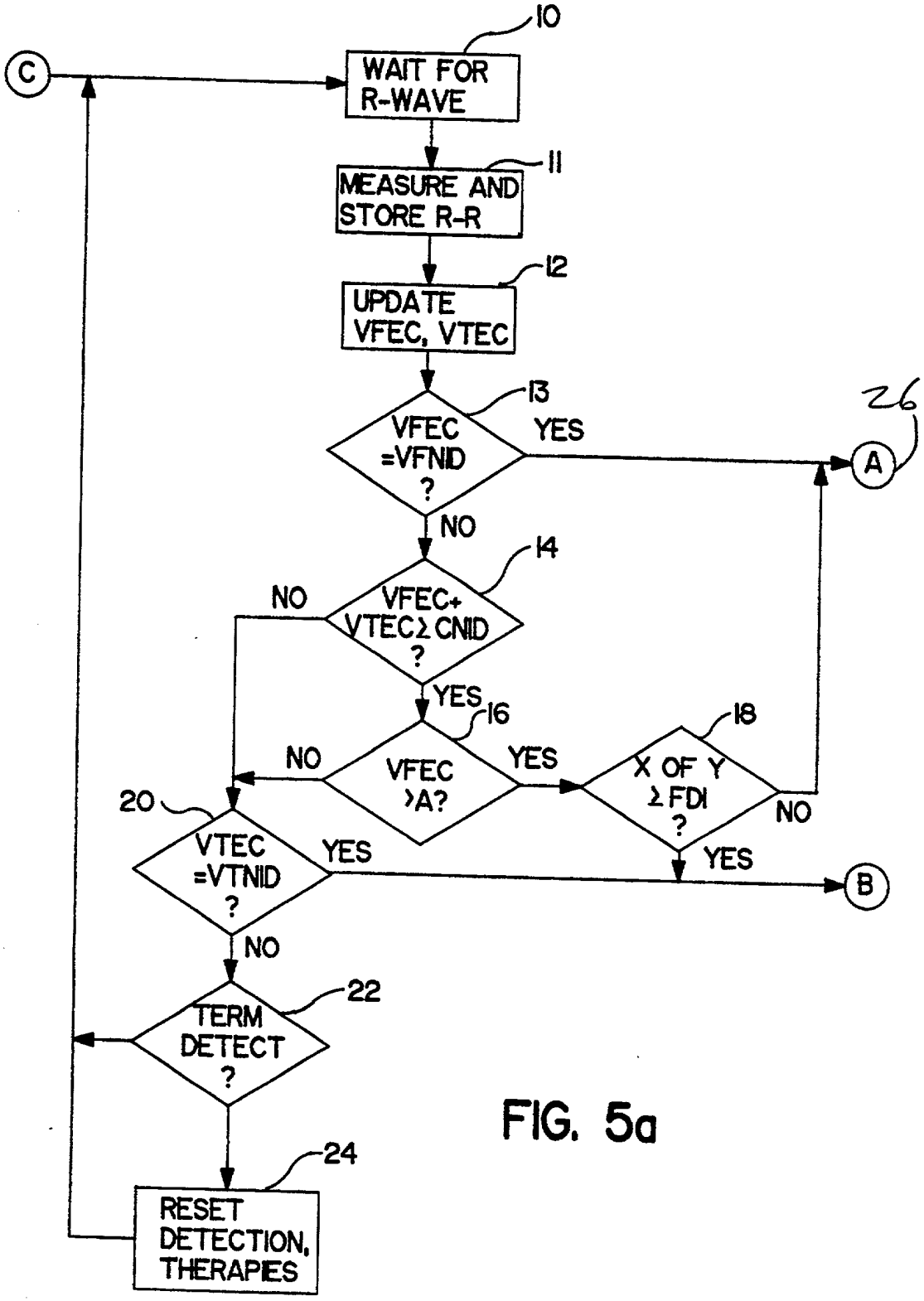
C
10
WAIT FOR R-WAVE
11
MEASURE AND STORE R-R
12
UPDATE VFEC, VTEC
13
VFEC =VFNID ?
YES
26
A
NO
14
VFEC+ VTEC≥ CNID ?
NO
YES
16
VFEC >A?
NO
YES
18
X OF Y ≥ FDI ?
NO
YES
20
VTEC =VTNID ?
YES
B
NO
22
TERM DETECT ?
24
RESET DETECTION, THERAPIES

FIG. 5a

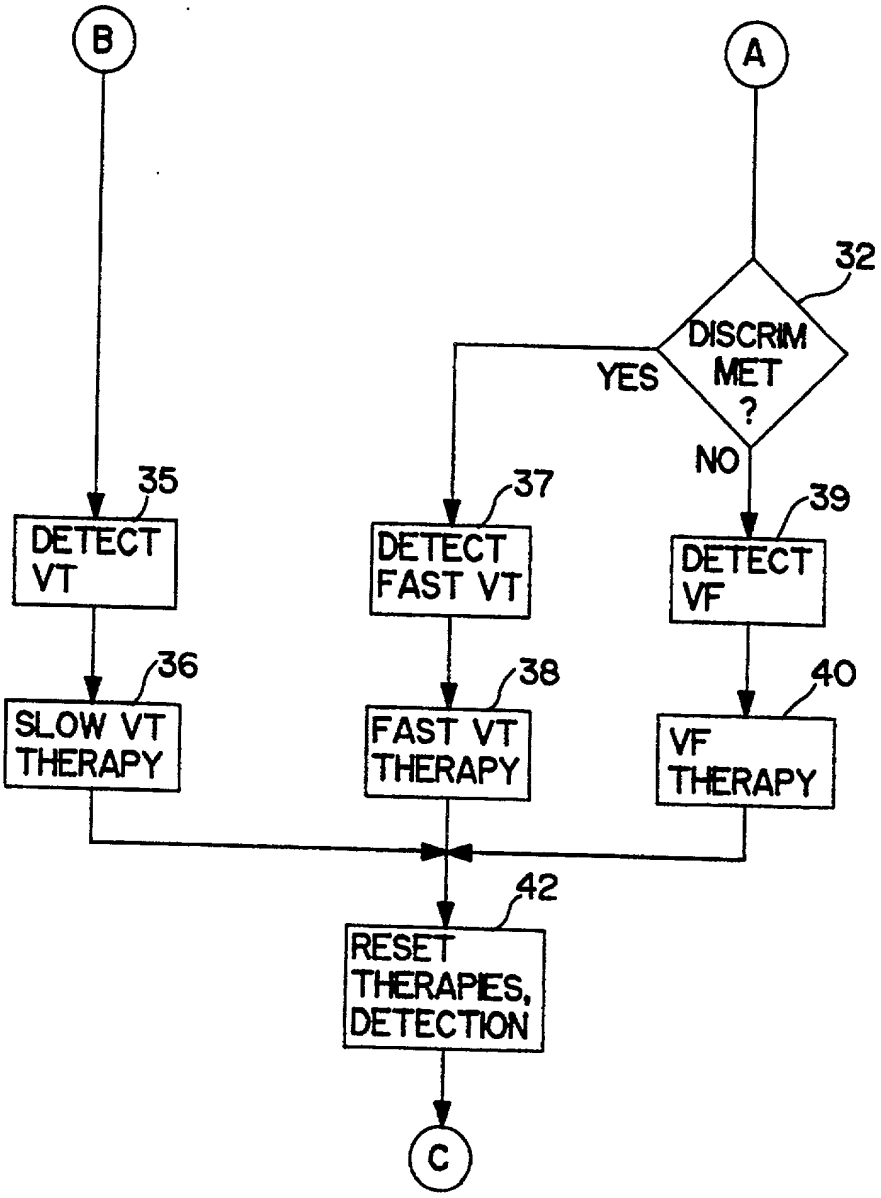
B
A
32
DISCRIM MET ?
YES
NO
35
DETECT VT
37
DETECT FAST VT
39
DETECT VF
36
SLOW VT THERAPY
38
FAST VT THERAPY
40
VF THERAPY
42
RESET THERAPIES, DETECTION
C

FIG. 5b